

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re patent application of:

Divakaruni et al.

Serial No. 09/718,850

Group Art Unit: 2811

Filed: November 11, 2000

Examiner: Gebremariam, Samuel A.

For: LOGIC STRUCTURE PROCESS AND APPLICATION FOR VERTICLE BIPOLAR TRANSISTOR

Assistant Commissioner of Patents  
PO Box 1450  
Alexandria, VA 22313-1450

## THIRD AMENDMENT UNDER 37 C.F.R. §1.111

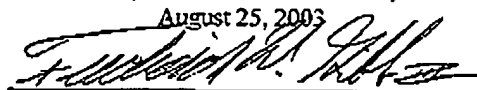
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Sir:

In response to the Office Action mailed January 22, 2003, the Notice of Non-Compliance dated May 16, 2003, the Examiner's telephonic request of August 21, 2003, and further to the First Amendment filed in April 22, 2003 and the Second Amendment filed on May 29, 2003, please amend the above-identified patent application as follows:

## Certificate of Transmission by Facsimile

I hereby certify that this correspondence is being facsimile transmitted to the United States Patent and Trademark Office (Fax No. 703-872-9318) on August 25, 2003.



Frederick W. Gibb, III